

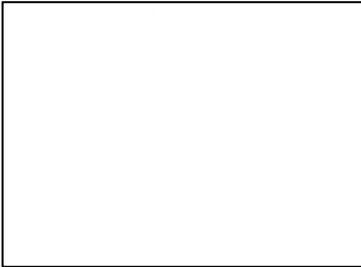
## Contents

1. Description	3
1.1 General Description	3
1.2 Features	3
1.3 Application	3
1.4 Package Dimension	4
1.5 Product Parameters	5
1.6 Bin Range Of Forward Voltage and Luminous Flux (IF=20mA)	BIN
(IF=20mA)	7
1.7 Typical Optical Characteristics Curves	8
2. Packaging	12
2.1 Packaging Specification	12
2.1.1 Carrier Tape Dimension	12
2.1.2 Reel Dimension	12
2.1.3 Label Form Specification	13
2.2 Moisture Resistant Packing	13
2.3 Cardboard Box	14
2.4 Reliability Test Items And Conditions	14
2.5 Criteria For Judging Damage	15
3. SMT Reflow Soldering Instructions SMT	16
3.1 SMT Reflow Soldering Instructions SMT	16
4. Handling Precautions	18
4.1 Handling Precautions	18



# 1. Description

## 1.1 General Description



The White LED, which was fabricated by using a blue chip and the phosphor.  
Product Package:1.8mmX0.8mmX0.50mm.

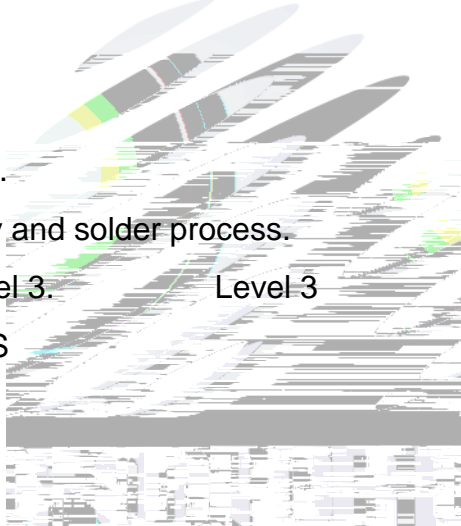
## 1.2 Features

Extremely wide viewing angle.

Suitable for all SMT assembly and solder process.

Moisture sensitivity level: Level 3. Level 3

RoHS compliant. RoHS



SMT

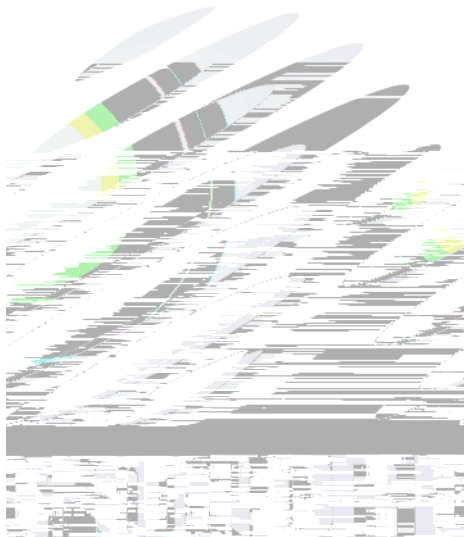
## 1.3 Application

Optical indicator.

Switch and Symbol, Display.

General use.





## 1.5 Product Parameters

Table 1-1 Electrical / Optical Characteristics at Ts=25°C

Item	Test Condition	Symbol	Value			Unit
			Min.	Typ. ( )	Max. ( )	
Spectral Half Bandwidth	$I_F=20\text{mA}$	$\Delta$	--	30	--	nm
Forward Voltage	$I_F=20\text{mA}$	$V_F$ 4I	2.7	--	3.1	V
Luminous Intensity	$I_F=20\text{mA}$	$I_v$ 2CB	1600	--	2500	mcd
Viewing Angle	$I_F=20\text{mA}$		--	120	--	deg
Reverse Current	$V_R=5\text{V}/10\text{ms}$	$I_R$	--	--	10	A
Thermal Resistance.	$I_F=20\text{mA}$	$R_{THJ-S}$	--	--	260	/W

Notes :  $V_R=5\text{V}$  For test conditions.  $V_R=5\text{V}$

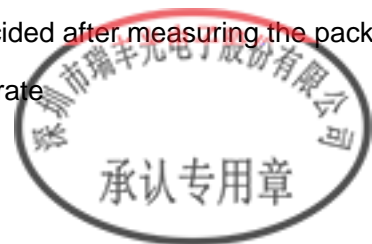


Table 1-2 Absolute Maximum Ratings at Ts=25°C

Parameter	Symbol	Rating	Units
Power Dissipation	Pd	93	mW
Forward Current	IF	30	mA
Peak Forward Current Of Pulse	IFP	60	mA
Electrostatic Discharge (HBM)	ESD	1000	V
Operating Temperature	Topr	-40 ~ +85	
Storage Temperature	Tstg	-40 ~ +85	
Junction Temperature	Tj	95	

## Notes

1. 1/10 Duty cycle, 0.1ms pulse width.
2. The above forward voltage measurement allowance tolerance is  $\pm 0.1V$ .
3. The above color coordinates measurement allowance tolerance is  $\pm 0.005$ .
4. The above luminous intensity measurement allowance tolerance  $\pm 10\%$ .
5. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
6. All measurements were made under the standardized environment of Refond.
7. When the LEDs are in operation the maximum current should be decided after measuring the package temperature, junction temperature should not exceed the maximum rate



### 1.6 Bin Range Of Forward Voltage and Luminous Flux (IF=20mA)

BIN (IF=20mA)

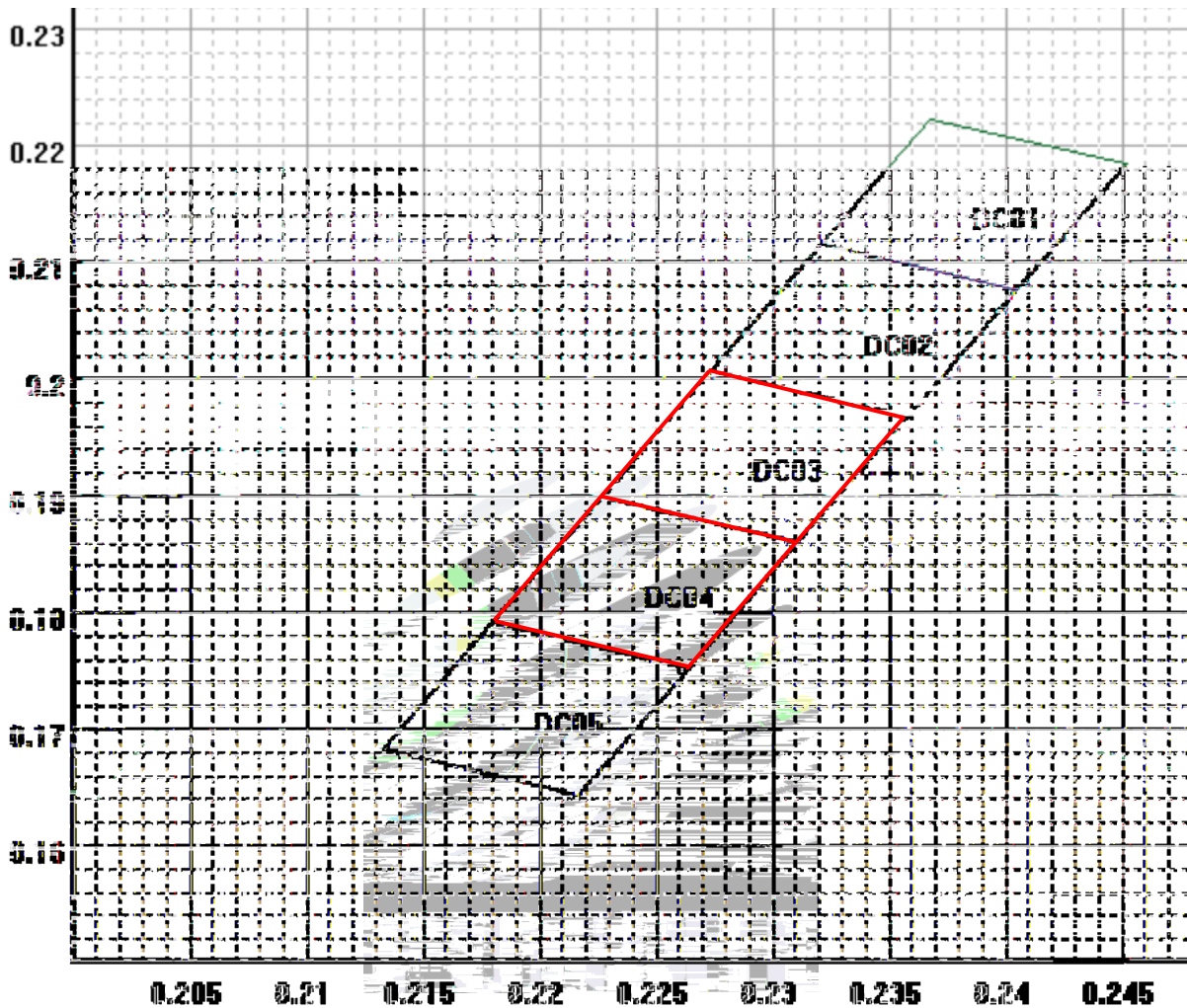


Fig. 1-6 The C.I.E Chromaticity Diagram CIE

Table 1-3 Bin Date Bin

BIN	X1	Y1	X2	Y2	X3	Y3	X4	Y4
DC01	0.2405	0.2075	0.2321	0.2114	0.2368	0.2222	0.2452	0.2183
DC02	0.2358	0.1967	0.2274	0.2006	0.2321	0.2114	0.2405	0.2075
DC03	0.2311	0.1859	0.2227	0.1898	0.2274	0.2006	0.2358	0.1967
DC04	0.2264	0.1751	0.218	0.179	0.2227	0.1898	0.2311	0.1859
DC05	0.2217	0.1643	0.2133	0.1682	0.218	0.179	0.2264	0.1751

### 1.7 Typical Optical Characteristics Curves

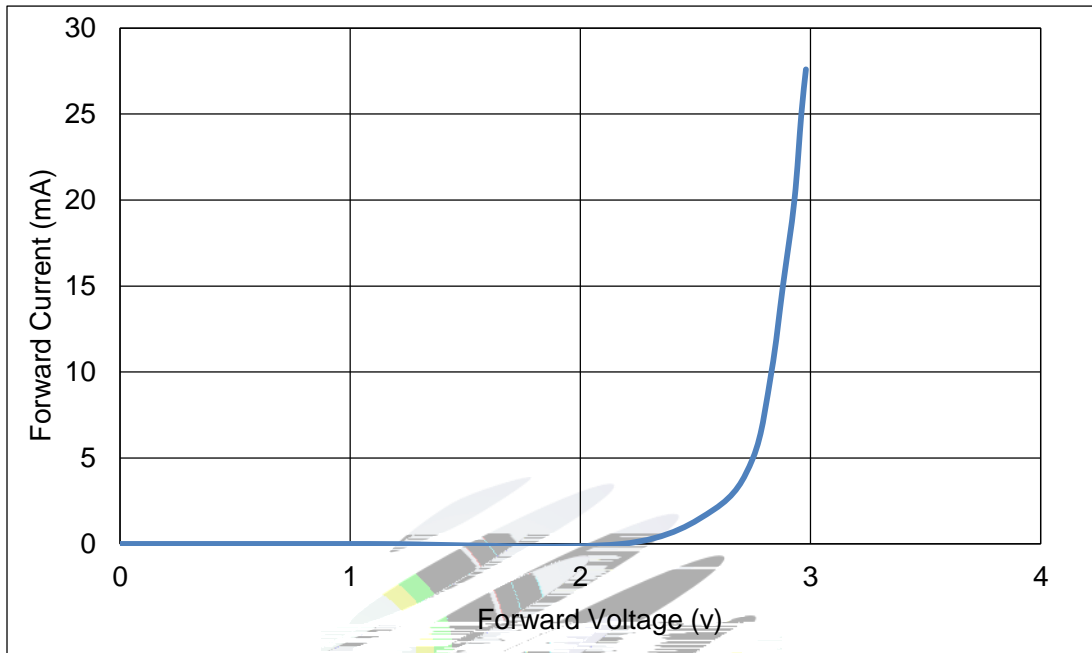


Fig. 1-7 Forward Voltage Vs Forward Current

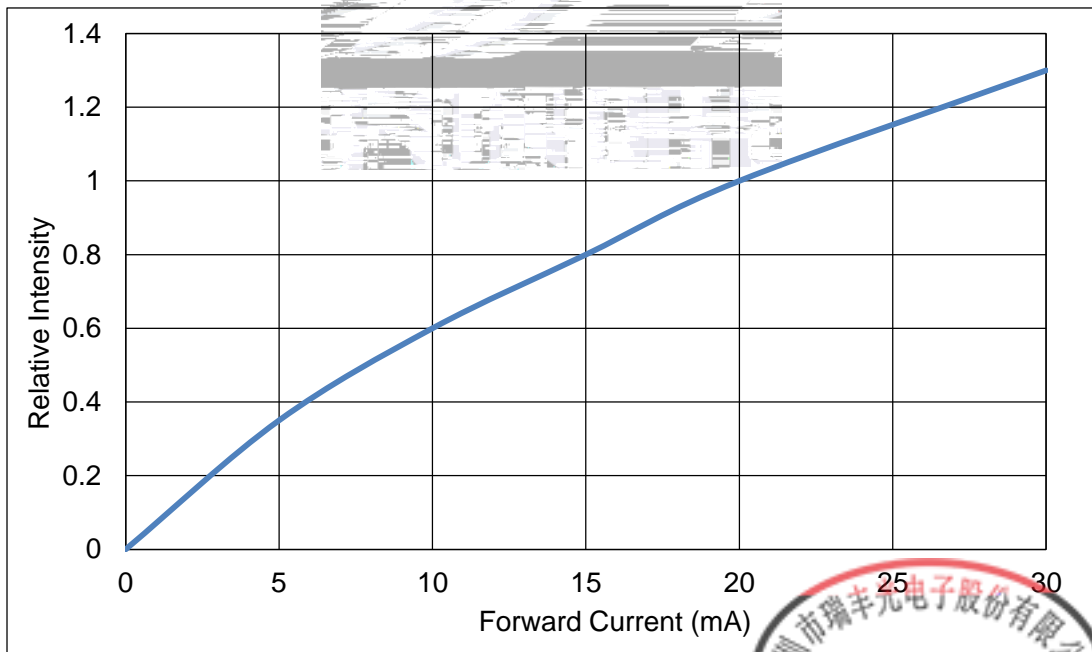
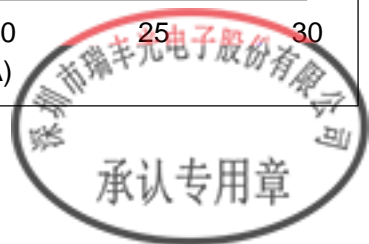


Fig. 1-8 Forward Current Vs Relative Intensity





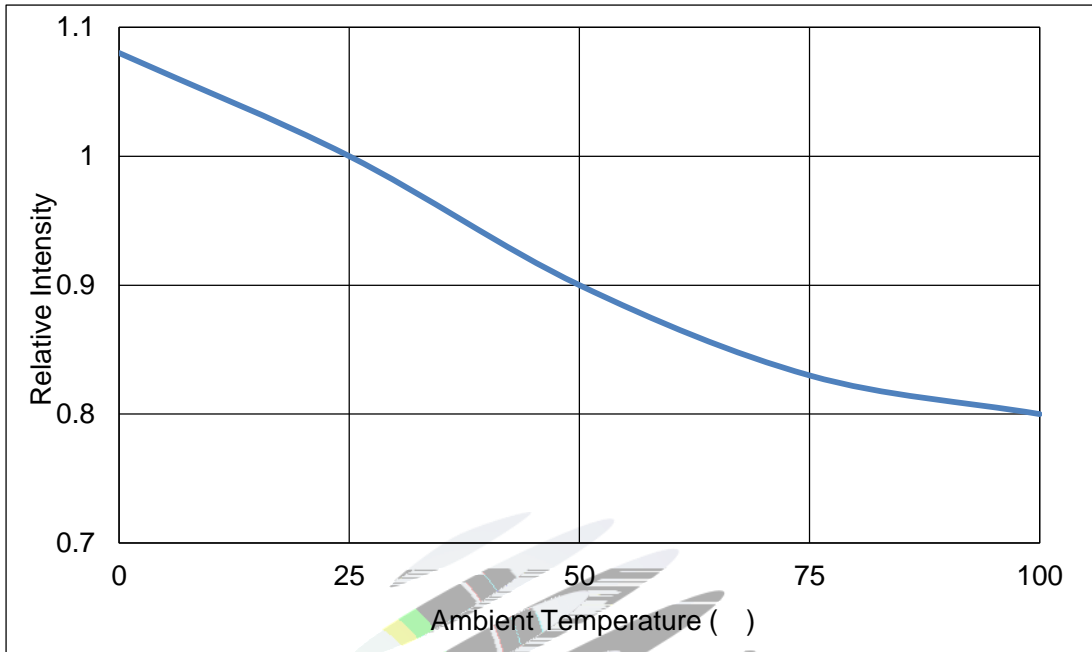


Fig. 1-9 Pin Temperature Vs Relative Intensity

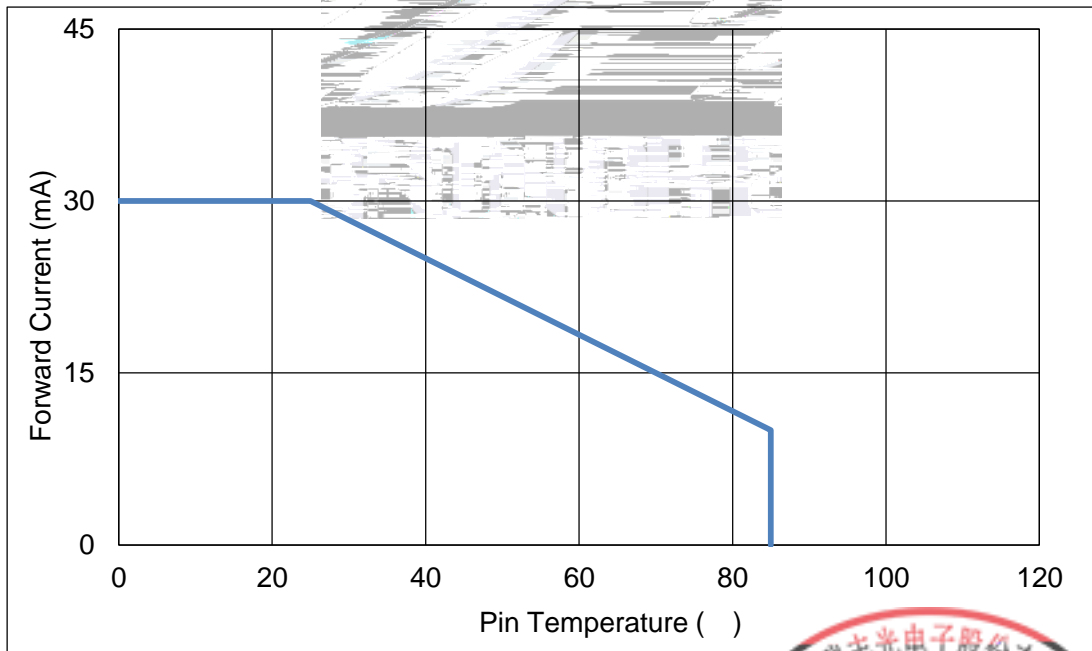
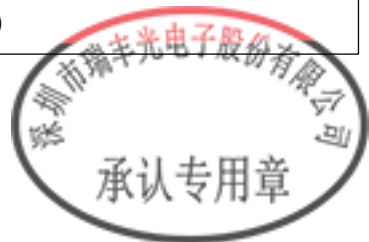


Fig. 1-10 Pin Temperature Vs Forward Current



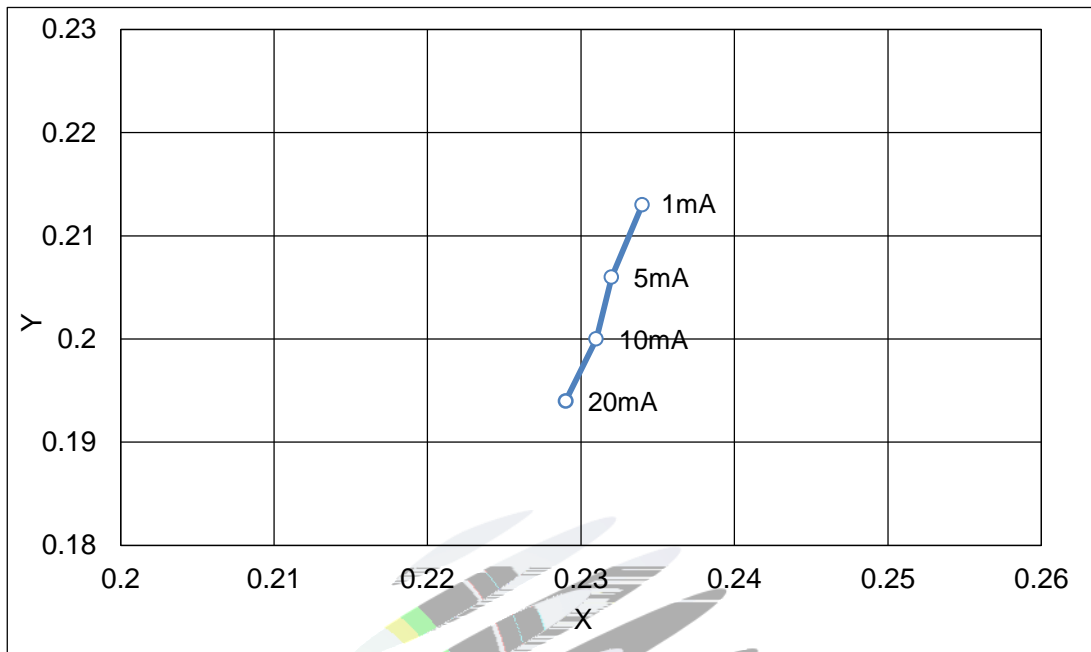


Fig. 1-11 Forward Current Vs Dominate Wavelength (Ta=25 )

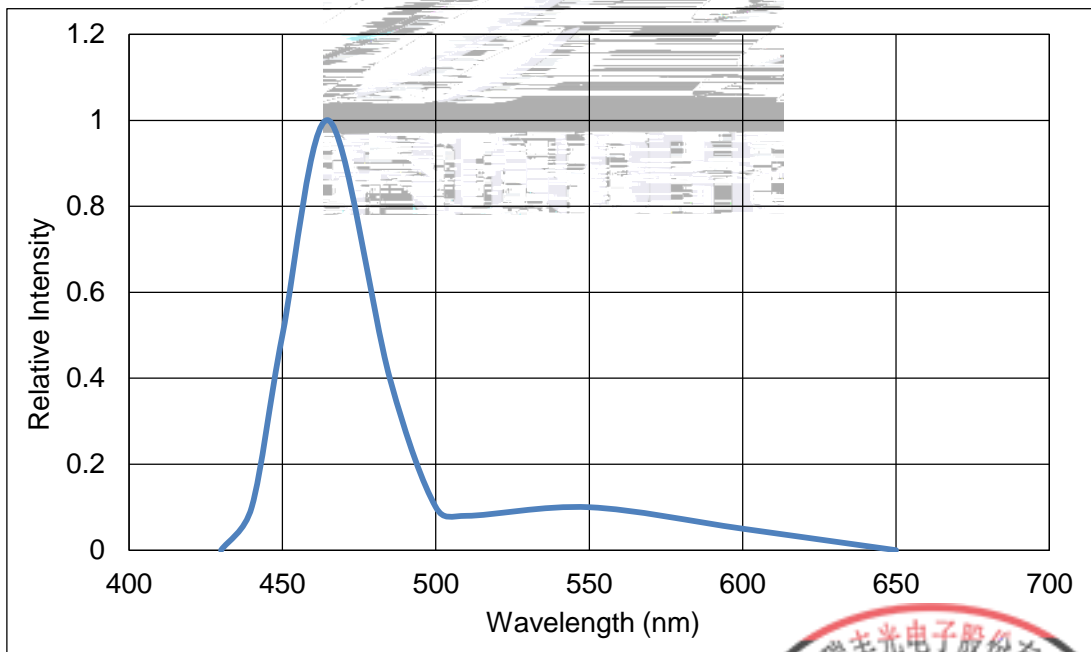
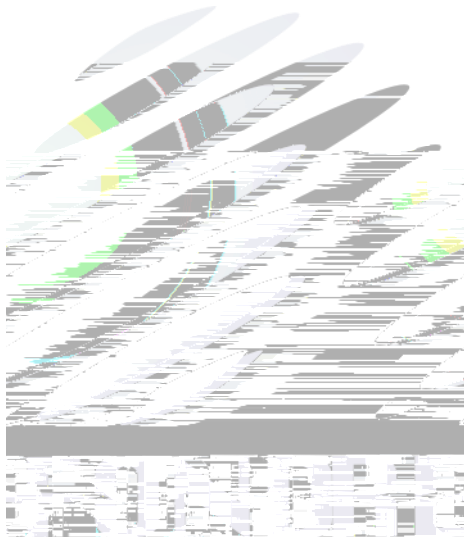


Fig. 1-12 Relative Intensity Vs Wavelength (Ta=25 )





## 2. Packaging

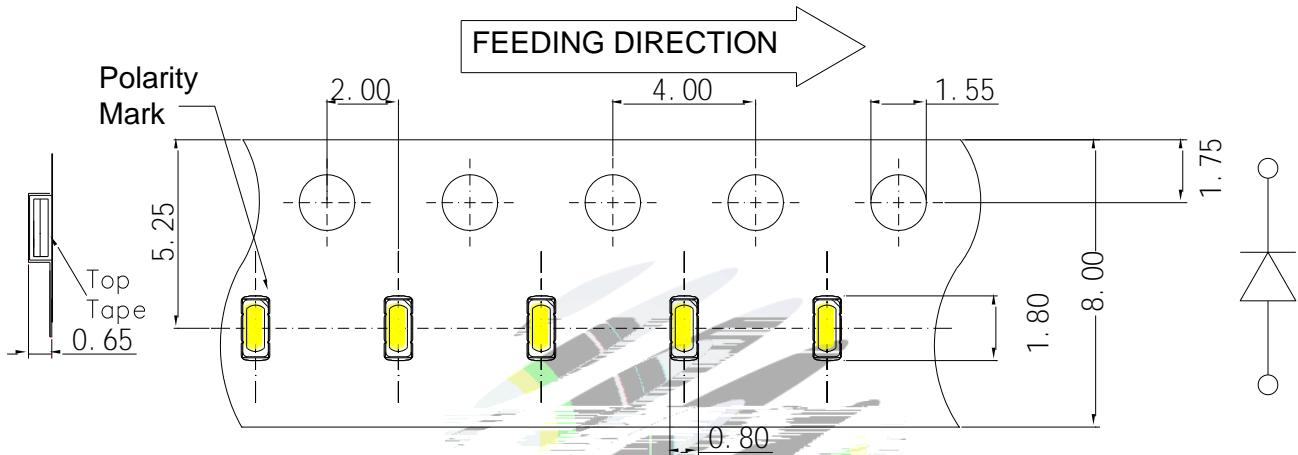
### 2.1 Packaging Specification

Package:20000pcs/reel.

20000pcs

#### 2.1.1 Carrier Tape Dimension

Fig.2-1 Carrier Tape Dimension



#### 2.1.2 Reel Dimension

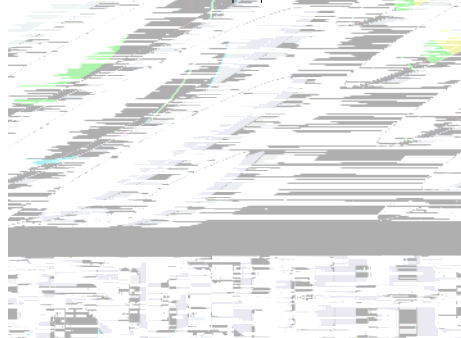


Table 2-1 Dimension

A	8.6 0.1mm
B	

Fig.2-2 Reel Dimension

#### Notes

The tolerances unless mentioned  $\pm 0.1\text{mm}$ . Unit : mm

### 2.1.3 Label Form Specification

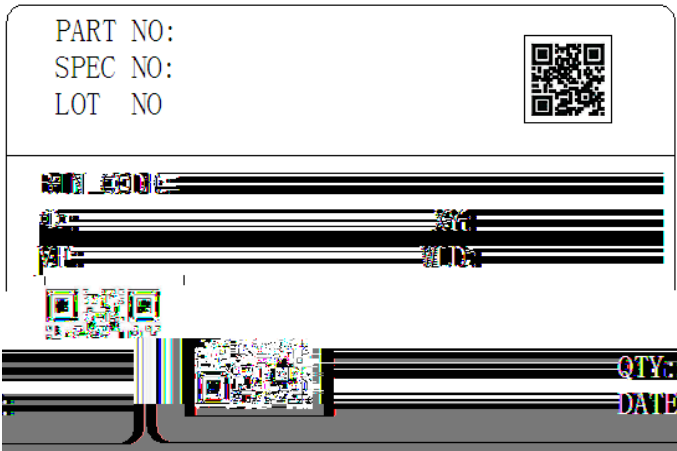


Table 2-2 Parameter

PART NO.	Part Number
SPEC NO.	Spec Number
LOT NO.	Lot Number
BIN CODE	Bin Code
	Luminous flux
XY	Chromaticity Bin
V <sub>F</sub>	Forward Voltage
WLD	Wavelength
QTY	Packing Quantity
DATE	Made Date

Fig. 2-3 Label Form Specification

### 2.2 Moisture Resistant Packing

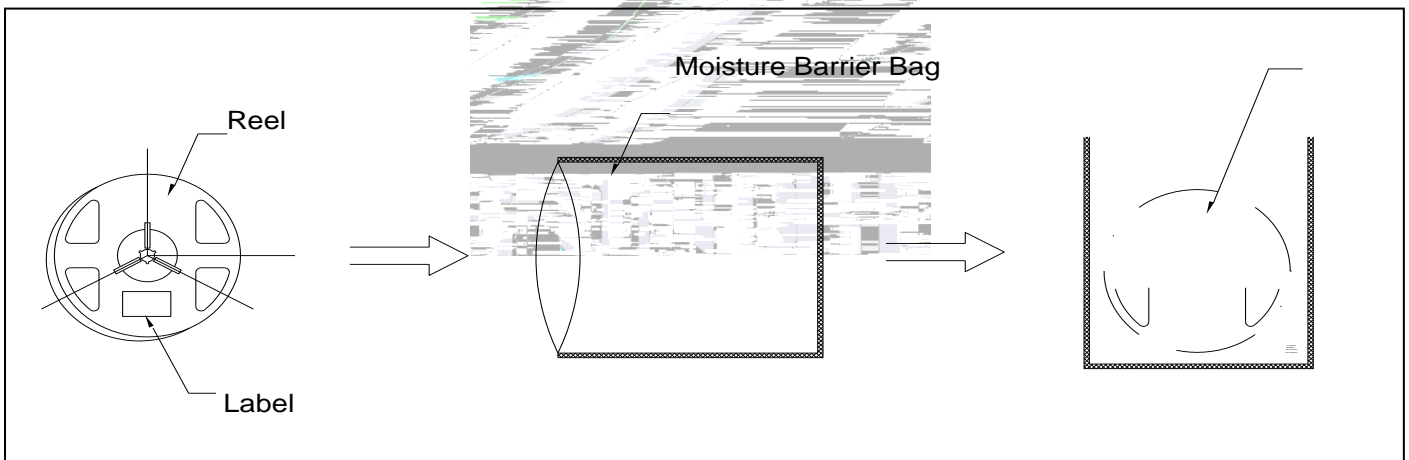


Fig.2-4 Moisture Resistant Packing



## 2.3 Cardboard Box

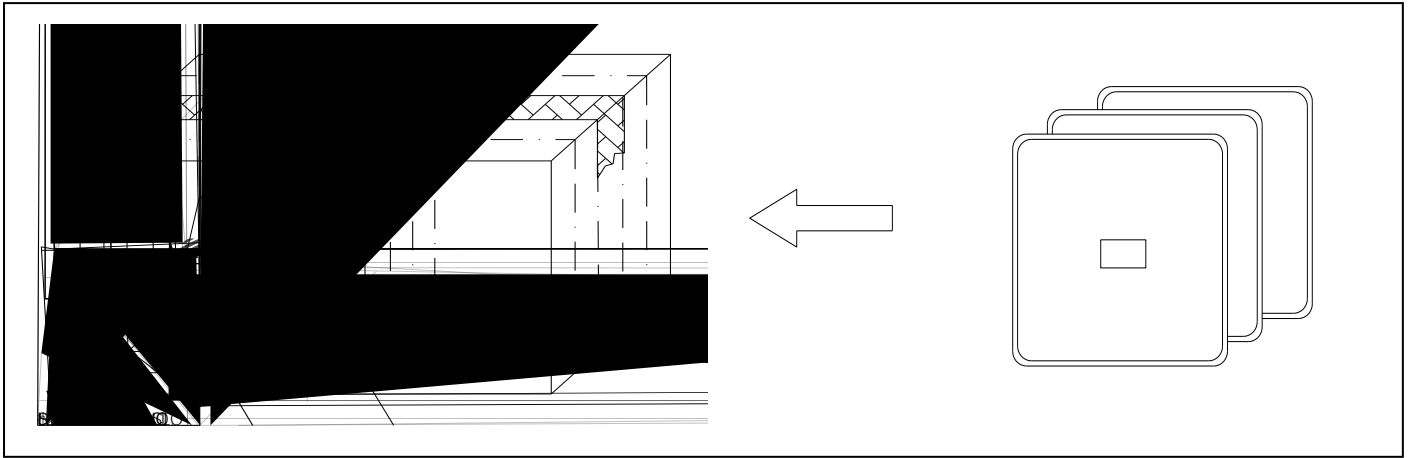
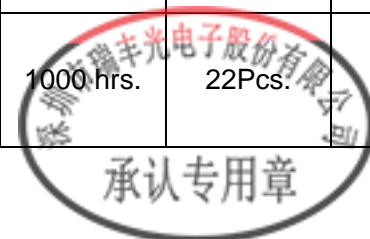


Fig.2-5 Cardboard Box

## 2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

Test Items	Ref.Standard	Test Condition	Time	Quantity	Ac/Re /
Reflow	JESD22-B106	Temp:260 max T=10 sec	2 times	22Pcs.	0/1
Temperature Cycle	JESD22-A104	100 30 min 5 min -40 30 min	100 cycles	22Pcs.	0/1
Thermal Shock	JESD22-A106	-40 15min 100 15min	300 cycles	22Pcs.	0/1
High Temperature Storage	JESD22-A103	Temp:100	1000 hrs.	22Pcs.	0/1
Low Temperature Storage	JESD22-A119	Temp:-40	1000 hrs.	22Pcs.	0/1
Life Test	JESD22-A108	T <sub>a</sub> =25 I <sub>F</sub> =20mA	1000 hrs.	22Pcs.	0/1



## 2.5 Criteria For Judging Damage

Table 2-4 Criteria For Judging Damage

Test Items	Symbol	Test Condition	Criteria For Judgement	
			Min.	Max.
Forward Voltage	$V_F$	$I_F=20mA$	-	U.S.L*)x1.1
Reverse Current	$I_R$	$V_R= 5V$	-	U.S.L*)x2.0
Luminous Flux		$I_F=20mA$	L.S.L*)x0.7	-

### Notes

1.U.S.L: Upper standard level

L.S.L: Lower standard level

2.The above reliability tests is based on the verification of a

BT/F3 10.54ETBBT348.5/F301064ETBBT454.03 301

78 1 3r-4(but)4(i)6(on,)]TJETBT1 0 0 1 81.624 279.65 TmC BT/F3 10.54ETBBT84.504 279.65 Tm 0.00864 Tc[heat)]TJE



### 3. SMT Reflow Soldering Instructions SMT

#### 3.1 SMT Reflow Soldering Instructions SMT

Fig.3-1 SMT Reflow Soldering Instructions SMT

Table 3-1 Parameter

Average temperature rise speed	$T_{smax}$ $T_P$	3 °C/ Max 3 °C/ s
Preheating: minimum temperature	( $T_{smin}$ )	150 °C
Preheating: Max temperature	( $T_{smax}$ )	200 °C
Preheating: Time	$T_{smin}$ $T_{smax}$	60 - 120 60s-120s
Time limited to maintain high temperature: the temperature	( $T_L$ )	217 °C

Time limited to maintain high temperature: The Time (t)

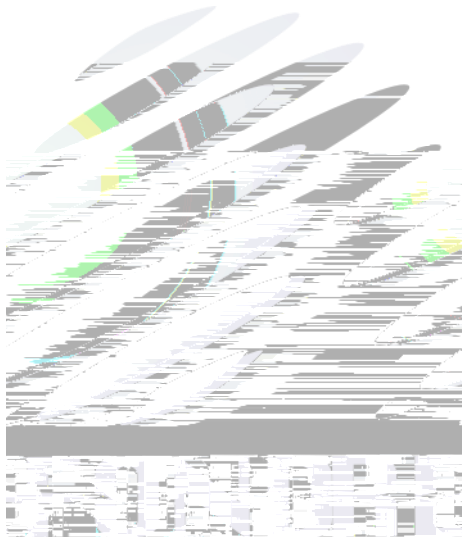




## 4. Handling Precautions

### 4.1 Handling Precautions

(1) LED operating environment and sulfur element composition cannot be over 100PPM in the



(4) Handle the component along the side surface by using forceps or appropriate tools; Do not directly touch or Handle the silicone lens surface, it may damage the internal circuitry.

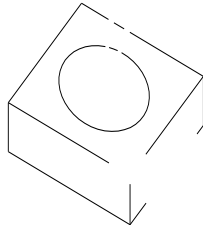
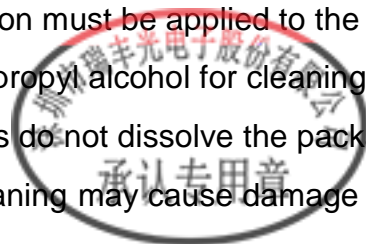


Fig 4-1 Handling Precautions

(5) In designing a circuit, the current through each LED can not exceed the absolute maximum rating specified for each LED. In the mean while, resistors for protection should be applied, otherwise slight voltage shift will cause big current change, burn out may happen. The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

(6) Thermal Design is paramount importance because heat generation may result in the Characteristics decline, such as brightness decreased, Color change and so on. Please consider the heat generation of the LEDs when making the system design. LED

(7) Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust, requiring special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components. Refond suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the



LED.

Table 4-1 Storage

Conditions		Temperature	Humidity	Time
Storage	Before Opening Aluminum Bag	30	75%	Within 1 Year From Date
	After Opening Aluminum Bag	30	60%	168hours 168
Baking		60 ± 5	-	24hours 24

(8) If the moisture absorbent material silica gel has faded away or the LEDs have exceeded the storage time, baking treatment should be performed after unpacking and based on the following condition 60±5 °C for above 24 hours.

If the package is flatulence or damaged, please notify the sales staff to assist.

(9) Similar to most Solid state devices; LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS).

(10) Other points for attention, please refer to our relevant information.







Declare

This specification is written both in English and in Chinese and the latter is formal.