

SPECIFICATION

REFOND P/N

RF-A1A30-WA02-A4

R&D

Mass Production



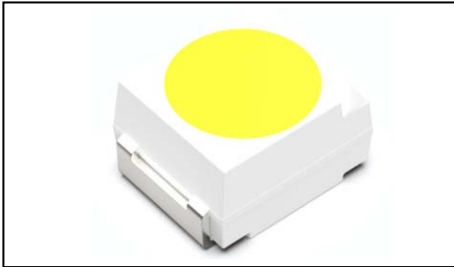
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1. Description

1.1 General Description



The White LED, which was fabricated by using a blue chip and the phosphor.

Product Package: 3.50mmX2.80mmX1.84mm.

LED

3.50mmX2.80mmX1.84mm.

1.2 Features

PLCC2 Package. PLCC2

Extremely wide viewing angle.

Suitable for all SMT assembly and solder process. SMT

Available on tape and reel.

Moisture sensitivity level: Level 2. Level2

Compliance with RoHS and REACH. RoHS REACH

Qualifications: The product qualification test plan is based on the guidelines of AEC-Q101 Stress Test Qualification for Automotive Grade Discrete Semiconductors

AEC-Q101

1.3 Application

Automotive Interior Lighting.

Switches.



1.5 Product Parameters

Table 1-1



Notes

1. 1/10 Duty cycle, 10ms pulse width. 10ms, 1/10.
2. The above forward voltage measurement allowance tolerance is $\pm 0.1V$. $\pm 0.1V$.
3. The above color coordinates measurement allowance tolerance is ± 0.005 . ± 0.005 .
4. The above luminous intensity measurement allowance tolerance $\pm 10\%$.
 $\pm 10\%$.
5. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
6. All measurements were made under the standardized environment of Refond.
7. When the LEDs are in operation the maximum current should be decided after measuring the package temperature, junction temperature should not exceed the maximum rate. LED
8. ESD yield is over 90% at 8000V ESD (HBM). ESD protection during products handling is needed. 90% LED
 ESD8000V

1.6 Bin Range Of Forward Voltage and Luminous Intensity (IF=5mA)
BIN (IF=5mA)

Table 1-3

| | | | | | | |
|------------------|---------|---------|---------|---------|---------|---------|
| V _F V | E2 | F1 | F2 | G1 | G2 | H1 |
| | 2.5-2.6 | 2.6-2.7 | 2.7-2.8 | 2.8-2.9 | 2.9-3.0 | 3.0-3.1 |
| IV(mcd) | J1 | J2 | K1 | | | |
| | 350-430 | 430-530 | 530-650 | | | |



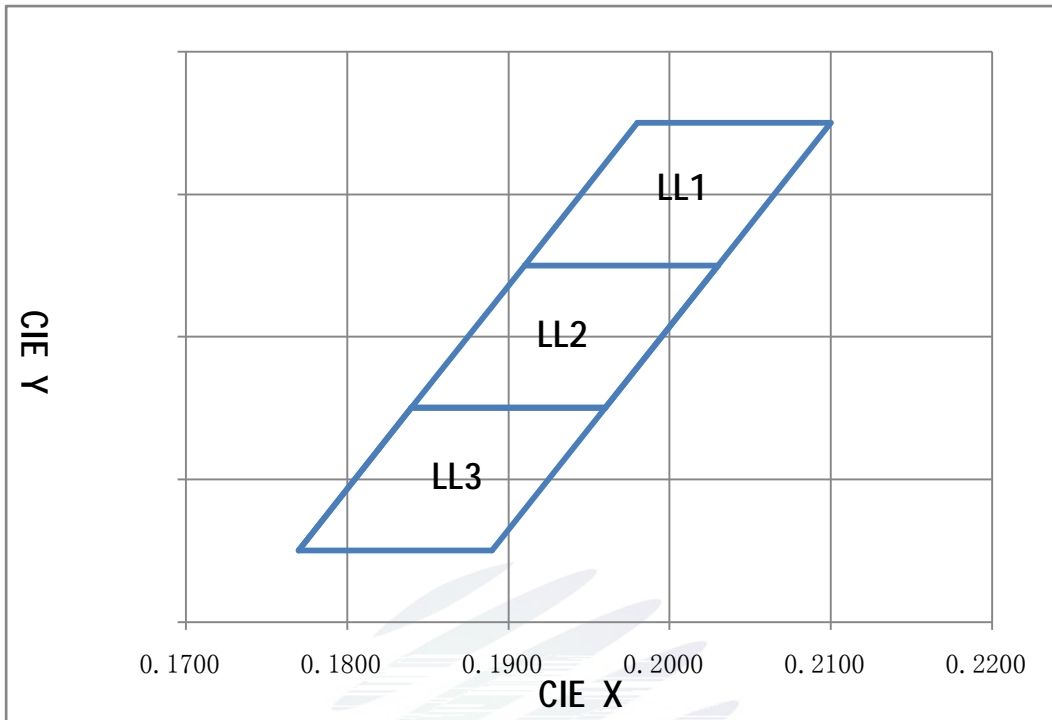


Fig. 1-6 The C.I.E Chromaticity Diagram CIE

Table 1-4

| BIN CODE | CIE-X1 | CIE-Y1 | CIE-X2 | CIE-Y2 | CIE-X3 | CIE-Y3 | CIE-X4 | CIE-Y4 |
|----------|--------|--------|--------|--------|--------|--------|--------|--------|
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |



1.7 Typical Optical Characteristics Curves

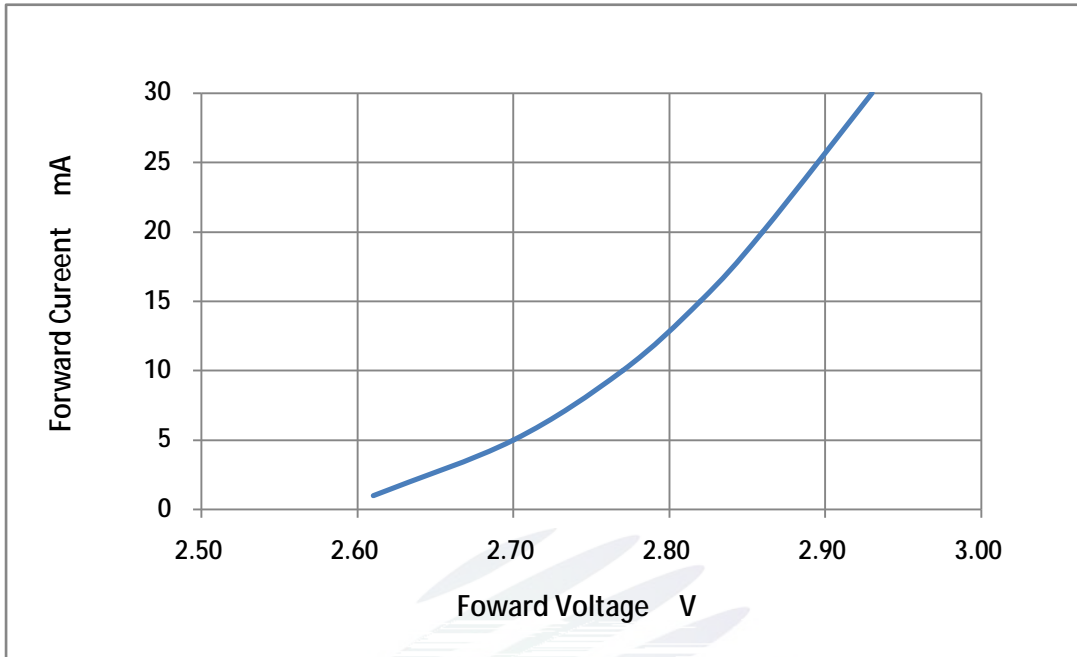


Fig. 1-7 Forward Voltage Vs Forward Current

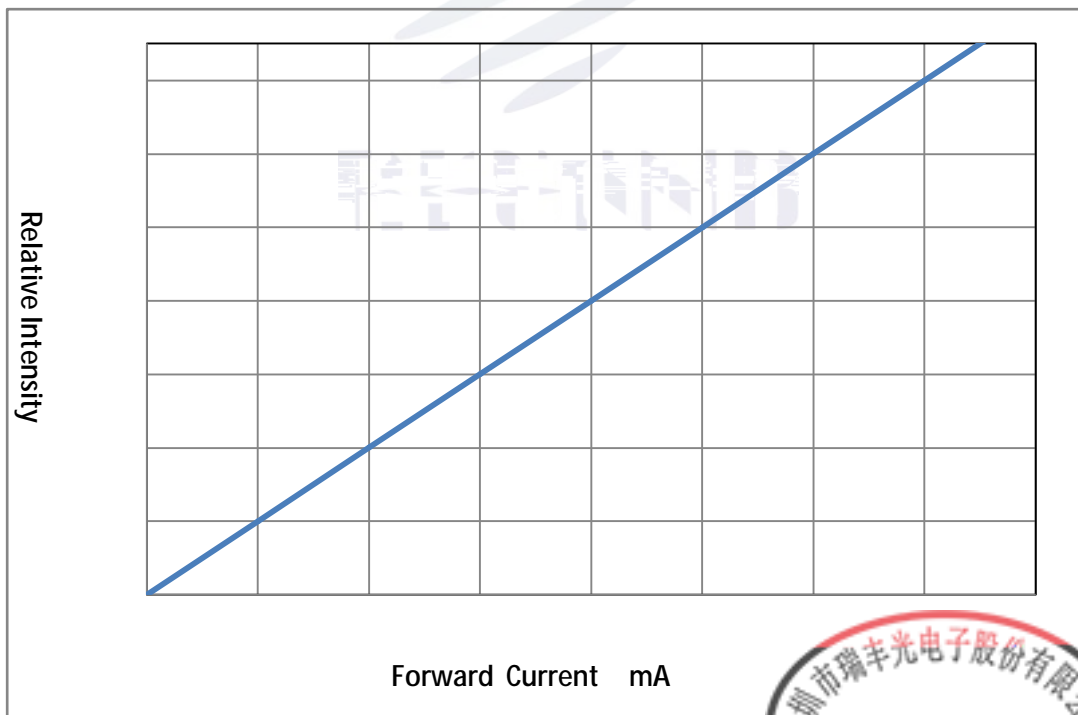


Fig. 1-8 Forward Current Vs Relative Intensity



Fig. 1-9





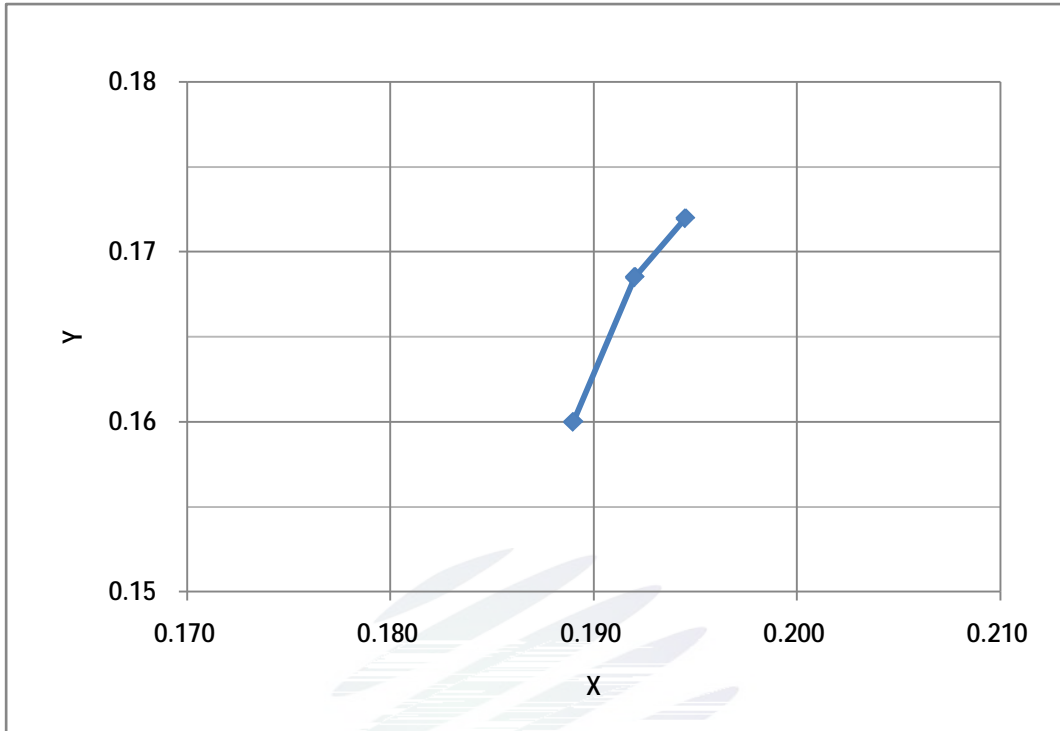


Fig. 1-13 Solder Temperature vs. Color Shift

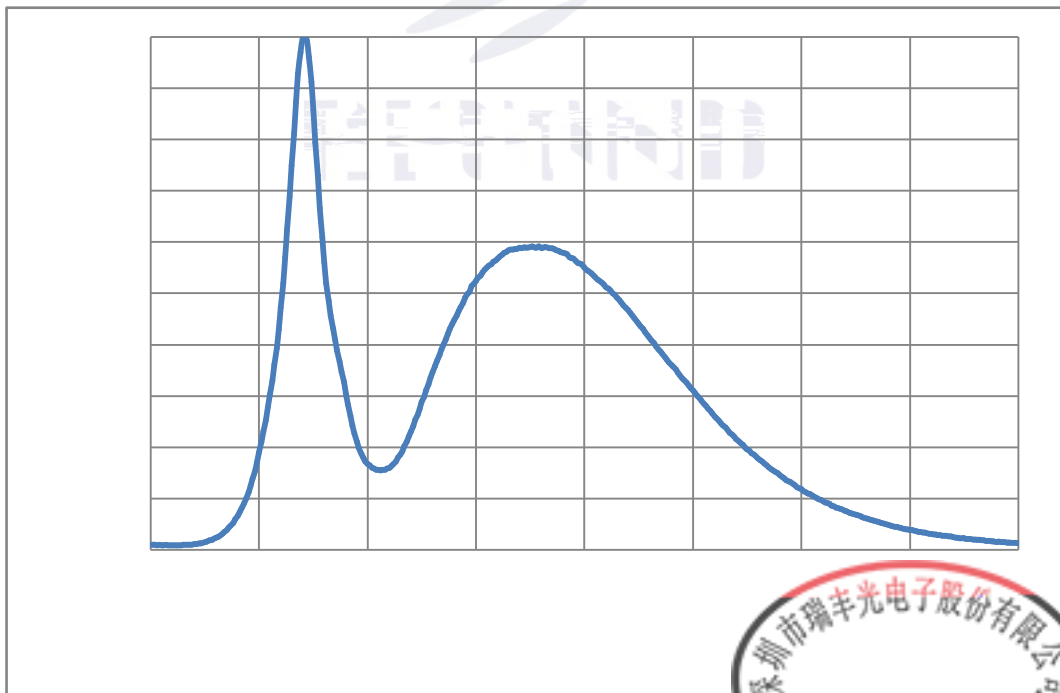


Fig. 1-14 Spectrum Distribution



2. Packaging

2.1 Packaging Specification

Package:2000pcs/reel.



2.1.3 Label Form Specification

Table 2-2 Specification

| | |
|----------|------------------|
| PART NO. | Part Number |
| SPEC NO. | Spec Number |
| LOT NO. | Lot Number |
| BIN CODE | Bin Code |
| | Luminous flux |
| XY | Chromaticity Bin |
| V_F | Forward Voltage |
| WLD | Wavelength |
| QTY | Packing Quantity |
| DATE | Made Date |

Fig. 2-3 Label Form Specification

2.2 Moisture Resistant Packing

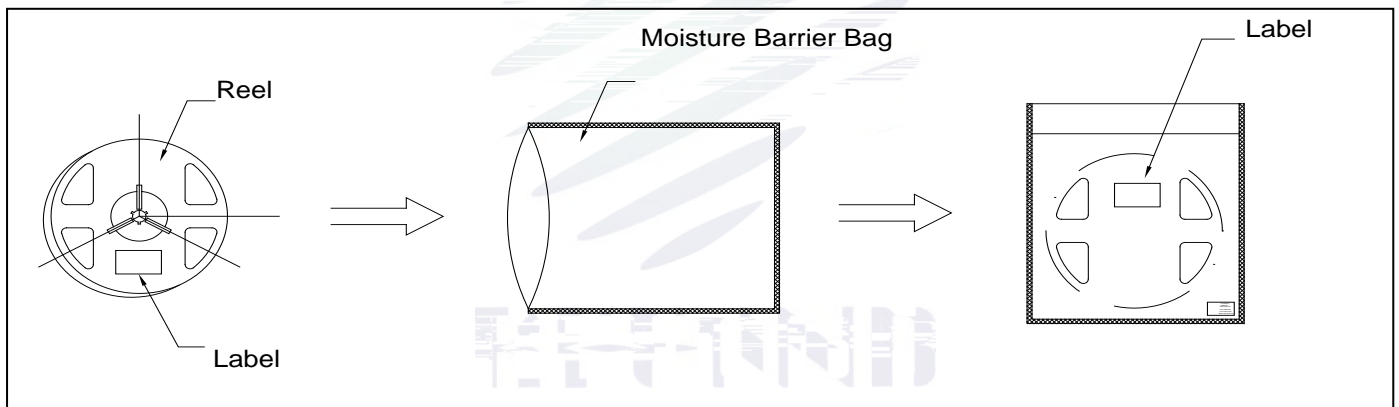


Fig.2-4 Moisture Resistant Packing

2.3 Cardboard Box

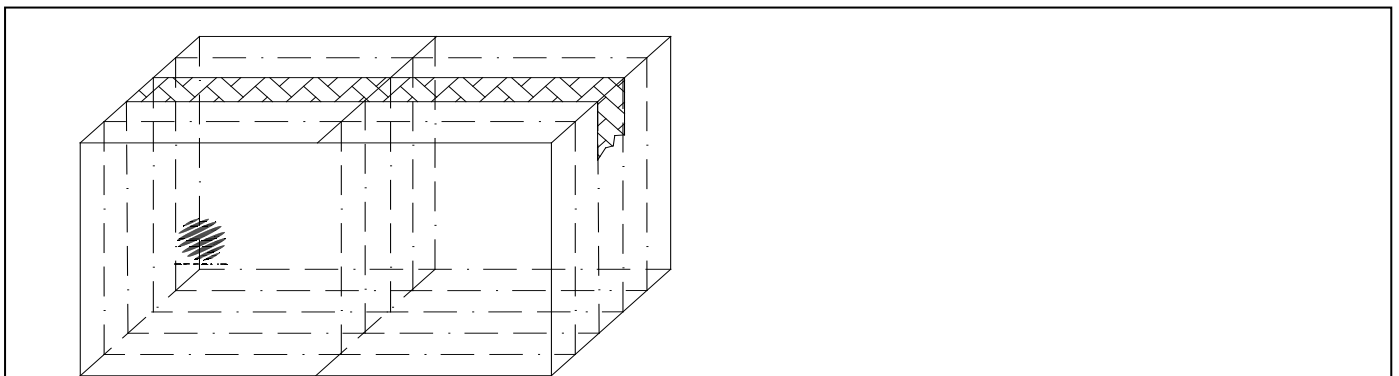


Fig.2-5 Cardboard Box

2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

| Test Items | Ref.Standard | Test Condition | Time | Quantity | Ac/Re / |
|---|------------------------|-------------------------------|------------|----------|------------|
| Reflow | JESD22-B106 | Temp:260 max T=10 sec | 2times | 20pcs. | 0/1 |
| MSL2 2 | JESD22-A113 | 85 / 60%RH | 168 hrs. | 20pcs. | 0/1 |
| Thermal Shock | JEITAED-4701 300307 | -40 15min 10s 125 15min | 1000 cycle | 20pcs. | 0/1 |
| Life Test | JESD22-A108 | Ta=100 If=5mA | 1000hrs. | 20pcs. | 0/1 |
| High Temperature High Humidity Life Test | JESD22-A101 | 85 / 85%RH If=5mA | 1000hrs. | 20pcs. | 0/1 |



2.5 Criteria For Judging Damage

Table 2-4 Criteria For Judging Damage

| Test Items | Symbol | Test Condition | Criteria For Judgement | |
|-----------------|--------|----------------|------------------------|-------------|
| | | | Min. | Max. |
| Forward Voltage | V_F | $I_F=5mA$ | - | U.S.L*)x1.1 |
| Reverse Current | I_R | $V_R = 5V$ | - | U.S.L*)x2.0 |
| Luminous Flux | | $I_F=5mA$ | L.S.L*)x0.7 | - |

Notes

- 1.U.S.L: Upper standard level L.S.L: Lower standard level
- 2.The above reliability tests is based on the verification of a single/strip LED of Refond's existing experimental platform,the reliability experiment was taken under good heat dissipation conditions. when customers applies the LED to the series and parallel circuit, should take consideration of all the factors such as the current, voltage distribution, heat dissipation and others. / LED
LED
- 3.The technical information shown in the data sheets is limited to the typical characteristics and circuit examples of the referenced products. It does not constitute the warranting of industrial property nor the granting of any license.



Notes

(1)Reflow soldering should not be done more than twice. If more than 24 hours between the two solderings , LED will be damaged. 24 LED

(2)When soldering , do not put stress on the LEDs during heating.

3.1.1 Soldering Iron

(1) When do soldering by hand, keep the temperature of iron below less 300 less than 3 seconds. 300 3

(2) Soldering by hand should be done only one time.

3.1.2 Repairing

Repairing should not be done after the LEDs have been soldered. When repairing is unavoidable,a double-head soldering iron should be used (as below figure). It should be confirmed in advance whether the characteristics of LEDs will or not be damaged by repairing.

LED

LED

3.1.3 Cautions

(1) The encapsulated material of the LEDs is silicone. Therefore the LEDs have a soft surface on the top of package. The pressure to the top surface will be impacted on the reliability of the LEDs. Precautions should be taken to avoid the strong pressure on the encapsulated part. So when use the picking up nozzle, the pressure on the silicone resin should be proper. LED LED

(2) Components should not be mounted on warped (non coplanar) portion of PCB. After soldering, do not warp the circuit board.LED PCB

(3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering. Do not rapidly cool device after soldering.

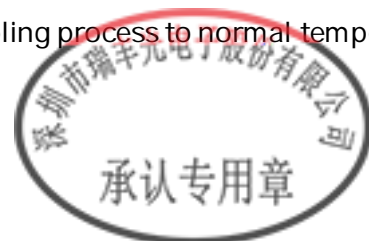
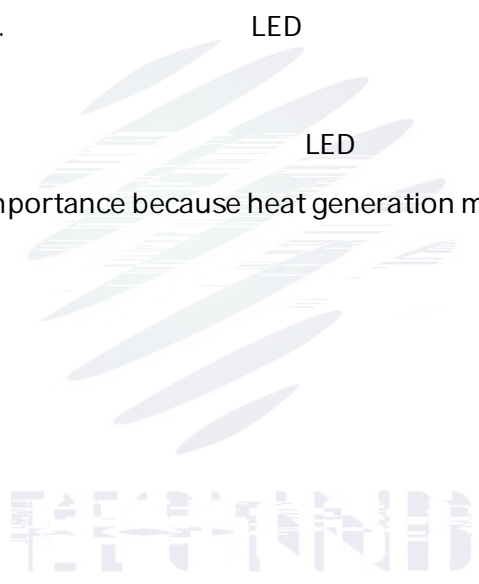


Fig 4-1 Handling Precautions

(5) In designing a circuit, the current through each LED can not exceed the absolute maximum rating specified for each LED. In the mean while, resistors for protection should be applied, other wise slight voltage shift will cause big current change, burn out may happen. The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.



(6) Thermal Design is paramount importance because heat generation may result in the Characteristics

d

Table 4-1 Storage

| Conditions | | Temperature | Humidity | Time |
|------------|-----------------------------|-------------|----------|-------------------------------------|
| Storage | Before Opening Aluminum Bag | 30 | 75% | Within 1 Year From Date |
| | After Opening Aluminum Bag | 30 | 60% | Recommended for use within 24 hours |
| Baking | | 60± 5 | - | 24hours 24 |

(8) If the moisture absorbent material silica gel has faded away or the LEDs have exceeded the storage time, baking treatment should be performed after unpacking and based on the following condition 60± 5 for above 24 hours.

60± 5 24

If the package is flatulence or damaged, please notify the sales staff to assist.

(9) Similar to most Solid state devices; LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS).
LED

(10) Other points for attention, please refer to our relevant information.





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Declare

This specification is written both in English and in Chinese and the latter is formal.